Call for Late News Papers

> ***Deadline for receipt of paper is February 2, 2024 23:59 (GMT+8)***
> A very limited number of high quality Late News Papers will be accepted.

REQUIREMENT

Authors are asked to submit late news papers announcing only very recent developments. PDF files should be in the same format as a regular paper (NO LESS or MORE THAN 2 PAGES) and should be emailed to the TSA secretariat, tsa@itri.org.tw. In order to make your paper receive a full review, the following information MUST be included in the email along with your submission:

- PDF file of your submitted paper
- 100~200 word abstract

SCOPE OF PAPER SOLICITED

- Front-end CMOS and Foundry technology
- Standalone memory: DRAM, FLASH, emerging memory technology
- Ultra-low power CMOS and embedded memory
- Advanced process modules: e.g. gate stack, junction, strain/channel engineering, low-R contact, low-C spacer/ILD, interconnect technology, ALE and selective deposition, etc.
- Nano-patterning: multiple patterning, directed self-assembly, EUV, etc.
- Advanced CMOS process and devices: Ge, SiGe, III-V, FinFET, GAAFET, low-dimensional materials and devices, 2D and nanowire devices
- BEOL compatible devices for 3D integration
- Material, Process and device modeling

- Reliability physics, characterization and test
- Advanced packaging and 2.5D/3D Integration
- TFT and organic electronics
- MEMS, imagers and sensors
- Power and analog IC device and technology
- Photonics and beyond CMOS Technology
- RF & THz process, device and integration technology
- Energy harvesting technology
- Wearable and IoE enabling technologies
- Quantum phenomena and information technologies
- Neuromorphic devices and materials for brain-inspired computing
- Device/circuit technologies for AI deep learning applications
- Advanced manufacturing technology, metrology and yield
- Emerging Materials and Devices

QUESTIONS ON PAPER SUBMISSION

For questions on paper submission, please contact:

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